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Amth B w/dups

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Leslie Charles Jenkins et al.

Serial No.: 09/239,323

Group Art Unit: 2829

Filed: January 28, 1999

Examiner: Nguyen, Vinh P.

For: INTEGRATED CIRCUITS FOR TESTING AN ACTIVE MATRIX DISPLAY ARRAY
(Amended)

Honorable Commissioner of Patents
Washington, D.C. 20231

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated August 30, 2002, please amend the above-identified application as follows:

RECEIVED
DEC-7 2002
TECHNOLOGY CENTER 2800

01/02/03 AM IN THE OFFICE 09239323

01/02/03 10.00 CH

Please change the title to "INTEGRATED CIRCUITS FOR TESTING AN ACTIVE
MATRIX DISPLAY ARRAY".

IN THE ABSTRACT:

B1
A device for use in a display system including an array of pixel cells formed on a substrate. Each pixel cell being coupled to at least one gate line of a plurality of gate lines formed on the substrate and at least one data line of a plurality of data lines being formed on the substrate. The device includes first and second transistors formed on the substrate. Each